

Programme Schedule & Speakers -

Session Day-1 March 18, 2024 (Monday)

Date	Time	Programme
March 18, 2024	09:00 – 09:30 AM	Registration
March 18, 2024	09:30 – 10:00 AM	Inauguration
March 18, 2024	10:00 – 11:30 AM	Keynote Talk Prof. Sung-Mo Steve Kang, University of California, Santa Cruz, USA Title of the Talk: VLSI Technology Evolution & Neuromorphic Computing
March 18, 2024	11:30 – 12:00 PM	Tea Break
March 18, 2024	12:00 – 01:30 PM	Prof. Sudeb Dasgupta, IIT Roorkee Title of the Talk: Low Power Design Techniques in Digital Domain
March 18, 2024	01:30 – 02:30 PM	Lunch Break
March 18, 2024	02:30 – 04:00 PM	Dr Anjan Kumar Pudi, Staff Engineer, Micron, India Title of the Talk: DRAM Array Design and Optimisation

Session Day-2 March 19, 2024 (Tuesday)

Date	Time	Programme
March 19, 2024	10:00 – 11:30 AM	Dr Sachin Taneja Research Scientist, Intel, Oregon, USA Title of the Talk: Energy-efficient and low-cost hardware security Primitives for secure ubiquitous computing
March 19, 2024	11:30 – 12:00 PM	Tea Break
March 19, 2024	12:00 – 01:30 PM	Dr Viveka Konandur Rajanna, IISc Bangalore Title of the Talk: Energy Efficient Computation for Machine Learning Workloads
March 19, 2024	01:30 – 02:30 PM	Lunch Break
March 19, 2024	02:30 – 04:00 PM	Dr Chongyan Gu, Queen's University Belfast, UK Title of the Talk: Secure Connected Devices

Session Day-3 March 20, 2024 (Wednesday)

Date	Time	Programme
March 20, 2024	10:00 – 11:30 AM	Dr Satya Gupta, President, VLSI Society of India, Founder of Open Silicon, Founder EPIC Title of the Talk: Making India a Semiconductor Product Nation
March 20, 2024	11:30 – 12:00 PM	Tea Break
March 20, 2024	12:00 – 01:30 PM	Dr Indranil Hatai, Senior Application Engineer, MathWorks Title of the Talk: Deep Dive into the Deep Hardware IP Design in FPGA by MathWorks Tool (Hands-on session)
March 20, 2024	01:30 – 02:30 PM	Lunch Break
March 20, 2024	02:30 – 04:00 PM	Dr Saswat Kumar Ram SRM University-AP, India (Hands-on session)

Session Day-4 March 21, 2024 (Thursday)

Date	Time	Programme
March 21, 2024	10:00 – 11:30 AM	Dr Shubham Sahay, IIT Kanpur Title of the Talk: Neuromorphic Computing: Mapping Neural Networks to Hardware
March 21, 2024	11:30 – 12:00 PM	Tea Break
March 21, 2024	12:00 – 01:30 PM	Dr Ramesh Vaddi, HoD ECE, SRM University-AP, India Title of the Talk: NCFET based SRAM Computing In-Memory Architectures for Energy Efficient Processing of DNN on Edge Devices
March 21, 2024	01:30 – 02:30 PM	Lunch Break
March 21, 2024	02:30 – 04:00 PM	Mr Anuj Walia, Senior Engineer ARM Title of the Talk: Strengthening Compute to Infra World with CXL System

Session Day-5 March 22, 2024 (Friday)

Date	Time	Programme
March 22, 2024	10:00 – 11:30 AM	Dr Krishna Kanth Avalur, Director Analog Mixed-Signal R&D, ams OSRAM, India Title of the Talk: Advances in Energy Efficient Sensors Interface Circuit
March 22, 2024	11:30 – 12:00 PM	Tea Break
March 22, 2024	12:00 – 01:30 PM	Dr M Durga Prakash, SRM University-AP, India (Hands-on session)
March 22, 2024	01:30 – 02:30 PM	Lunch Break
March 22, 2024	02:30 – 04:00 PM	Closing Ceremony and Feedback